MT4435A

P-Channel Enhancement Mode Field Effect Transistor

Product Summary

PRODUCT SUMMARY					
Vdss	ΙD	$RDS(ON)$ $(m \Omega)$ Typ			
-30V	-7A	20 @VGS =-10V			
-30 v	-/A	30 @V GS=-4.5V			

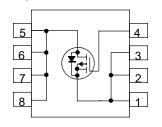
Features

- Supper high dense cell design for low RDS(ON)
- · Rugged and reliable
- · Simple drive requirement
- SO-8 Package

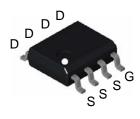
MT Semiconductor®

http://www.mtsemi.com

Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



SO-8

Absolute Maximum Ratings(T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	-30	V	
Gate-Source Voltage	VGS	±20	V	
Drain Current-Continuous ^a @Tj=125 ℃	ID	-7	A	
- Pulse d^b	IDM	-25	A	
Drain-source Diode Forward Current ^a	Is	-7.0	A	
Maximum Power Dissipation ^a	PD	2.5	W	
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 to 150	°C	

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to Ambient ^a	Rth JA	50	°C/W

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit	
OFF CHARACTERISTICS					1		
Drain-Source Breakdown Voltage	BVDSS	V _{GS} =0V,I _D =-250μA	-30			V	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-24V,V _{GS} =0V			-1	μΑ	
Gate-Body Leakage	Igss	V _{GS} =±20V,V _{DS} =0V			±100	nA	
ON CHARACTERITICS							
Gate Threshold Voltage	V _G s(th)	$V_{DS}=V_{GS},I_{D}=-250\mu A$	-1	-1.7	-2.5	V	
Drain-Source On-State Resistance	Drawaya	V _{GS} =-10V,I _D =-8A		20	22	- m Ω	
Drain-Source On-State Resistance	Rds(on)	V _{GS} =-4.5V,I _D =-5.0A		30	35		
Forward Transconductance	gFS	V _{GS} =-15V,I _D =-8A		6		S	
DAYNAMIC CHARACTERISTICS				I	1		
Input Capacitance	Ciss			1119		pF	
Output Capacitance	Coss	$V_{DS}=-15V, V_{GS}=0V$ f=1.0MHz		363		pF	
Reverse Transfer Capacitance	Crss	T T.OWILE		138		pF	
SWITCHING CHARACTERISISTICS							
Turn-On Delay Time	tD(ON)	V _{DD} =-15V		17.8		ns	
Rise Time	tr	ID=-8A,		17.5		ns	
Turn-Off Delay Time	td(off)	V _{GEN} =-4.5V R _L =10ohm		169		ns	
Fall Time	tf	RGEN=60hm		96		ns	
Total Gate Charge	Qg			35		nC	
Gate-Source Charge	Qgs	Vds=-15V,Id=-1A Vgs=-10V		3.3		nC	
Gate-Drain Charge	Qgd	ν Ω2—-10 ν		8.1		nC	

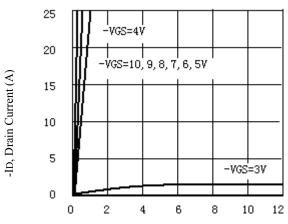
ELECTRICAL CHARACTERICS (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage	Vsd	V _{GS} =0V,I _S =-1.7A		-0.74	-1.2	V

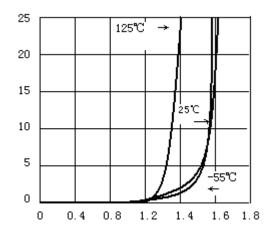
-ID, Drain Current(A)

Notes

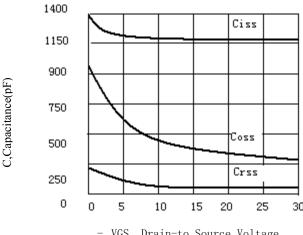
- a. Surface Mounted on FR4 Board, t ≤ 10sec
- b. Pulse Test: Pulse Width ≤ 300Us, Duty ≤ 2%
- c. Guaranteed by design, not subject to production testing.



- VDS, Drain-to-Source Voltage (V) Figure 1.Output Characteristics



-Vcs, Gate-to-source Voltage (V) Figure 2. Transfer Characteristics



VGS, Drain-to Source Voltage Figure 3. Capacitance

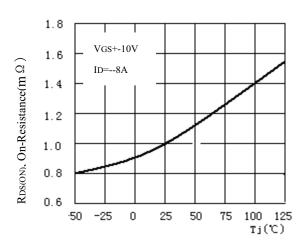
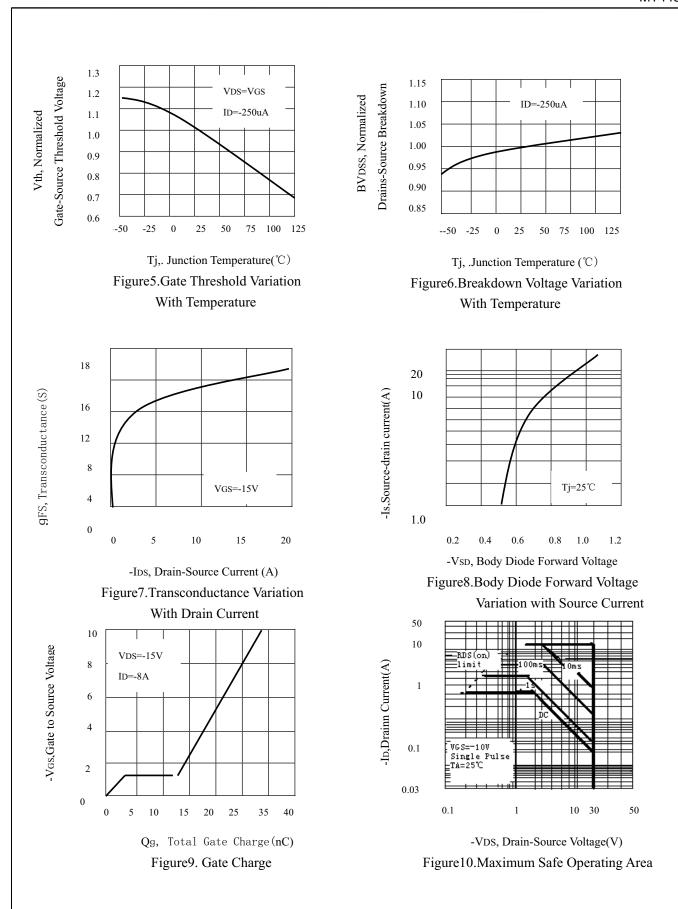


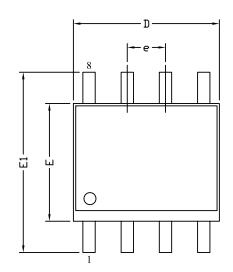
Figure 4. On-Resistance Variation with Temperature

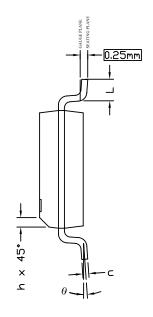


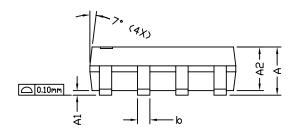
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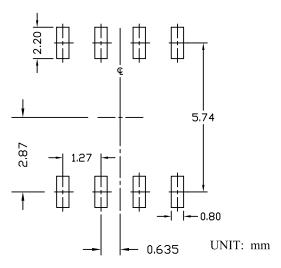
SO8 PACKAGE OUTLINE







RECOMMENDED LAND PATTERN



CVMDOLC	YMBOLS DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
31 MBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	1.35	1.65	1.75	0.053	0.065	0.069	
A1	0.10		0.25	0.004		0.010	
A2	1.25	1.50	1.65	0.049	0.059	0.065	
ь	0.31		0.51	0.012		0.020	
С	0.17		0.25	0.007		0.010	
D	4.80	4.90	5.00	0.189	0.193	0.197	
Е	3.80	3.90	4.00	0.150	0.154	0.157	
e	1.27 BSC			0.050 BSC			
E1	5.80	6.00	6.20	0.228	0.236	0.244	
h	0.25		0.50	0.010		0.020	
L	0.40		1.27	0.016		0.050	
θ	00		80	00		80	

NOTE

- 1. ALL DIMENSIONS ARE IN MILLMETERS.
- 2. DIMENSIONS ARE INCLUSIVE OF PLATING.
- 3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.

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- 4. DIMENSION L IS MEASURED IN GAUGE PLANE.
- 5. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

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