

MT3274S

N-Channel Power MOSFET
40 V, 70 A, 6.5 mΩ

Features

- Typ $R_{DS(on)} = 6.5\text{ m}\Omega$ at $V_{GS} = 10\text{ V}$
- High performance trench technology for extremely low $R_{DS(on)}$
- High power and current handling capability

General Description

This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low $R_{DS(ON)}$ and fast switching speed.

Applications

- DC-DC primary bridge
- DC-DC Synchronous rectification
- Power Management for Inverter Systems

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (T _A =25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		40	V
V _{GSS}	Gate-Source Voltage		± 20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	°C
I _S	Diode Continuous Forward Current	T _C =25°C	70	A

Mounted on Large Heat Sink

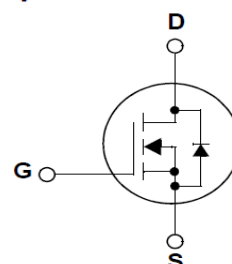
I_{DM}	Pulsed Drain Current *		310	A
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	70	A
		$T_C = 100^\circ\text{C}$	41	
P_D	Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	55	W
		$T_C = 100^\circ\text{C}$	28	
$R_{\theta JC}$	Thermal Resistance-Junction to Case		1.6	$^\circ\text{C/W}$



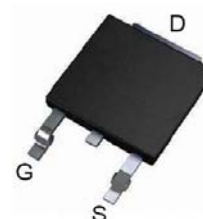
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Simplified Schematic



MARKING DIAGRAM & PIN ASSIGNMENT



TO-252-2L

Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

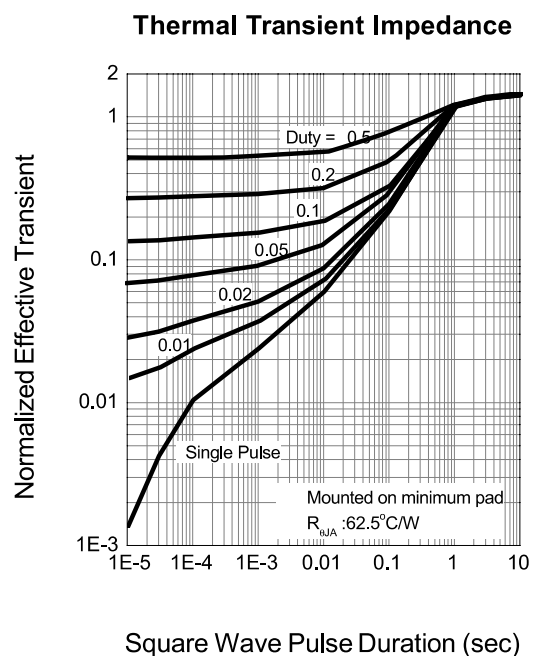
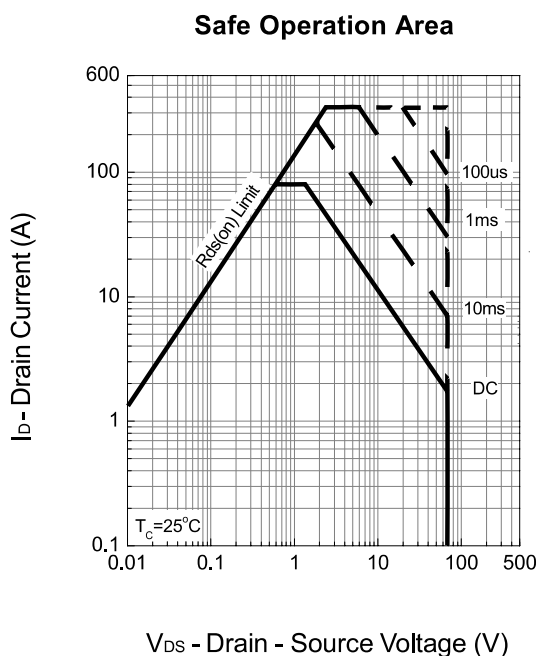
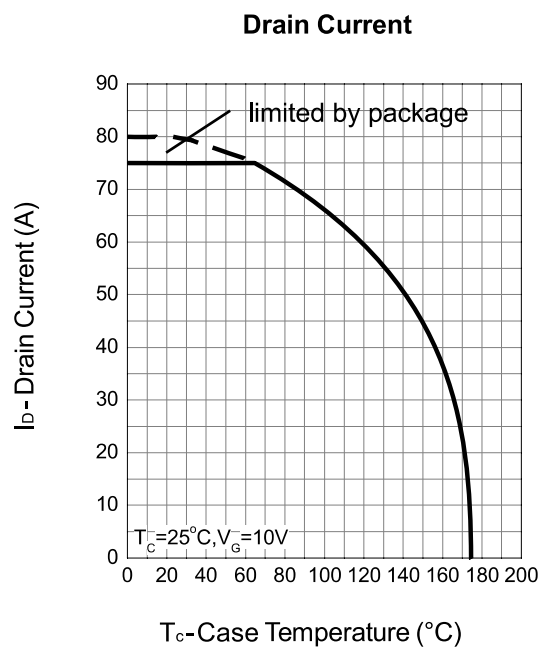
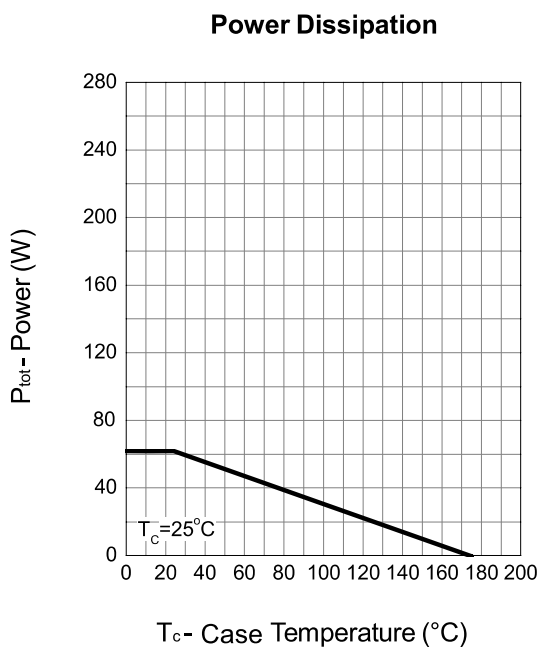
Symbol	Parameter	Test Conditions				Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	10	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1	1.6	3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} *	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =20A	-	6.5	8.5	mΩ
Diode Characteristics						
V _{SD} *	Diode Forward Voltage	I _{SD} =20A, V _{GS} =0V	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =20A, dI _{SD} /dt=100A/μs	-	13	-	ns
Q _{rr}	Reverse Recovery Charge		-	12	-	nC

Electrical Characteristics (Cont.) ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions				Unit
			Min.	Typ.	Max.	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	3.0	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =20V, Frequency=1.0MHz	-	3310	-	pF
C _{oss}	Output Capacitance		-	257	-	
C _{rss}	Reverse Transfer Capacitance		-	186	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =20V, R _G =3 Ω, I _{DS} =30A, V _{GS} =10V,	-	13	-	ns
T _r	Turn-on Rise Time		-	30	-	
t _{d(OFF)}	Turn-off Delay Time		-	62	-	
T _f	Turn-off Fall Time		-	18	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =20V, I _{DS} =30A	-	71	-	nC
Q _{gs}	Gate-Source Charge		-	16	-	
Q _{gd}	Gate-Drain Charge		-	17	-	

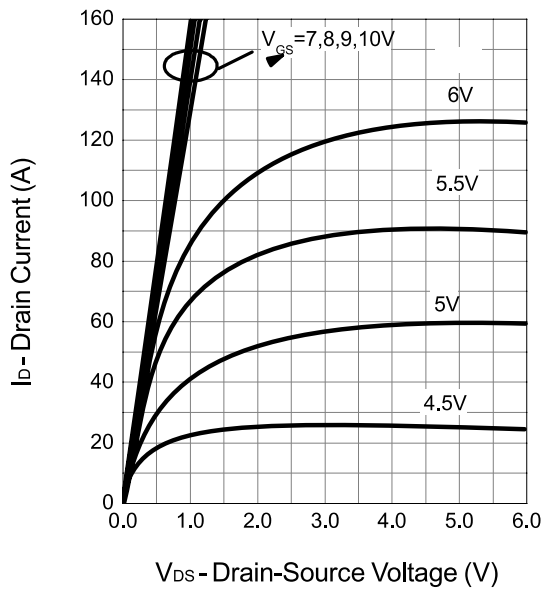
Note * : Pulse test ; pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.

Typical Operating Characteristics

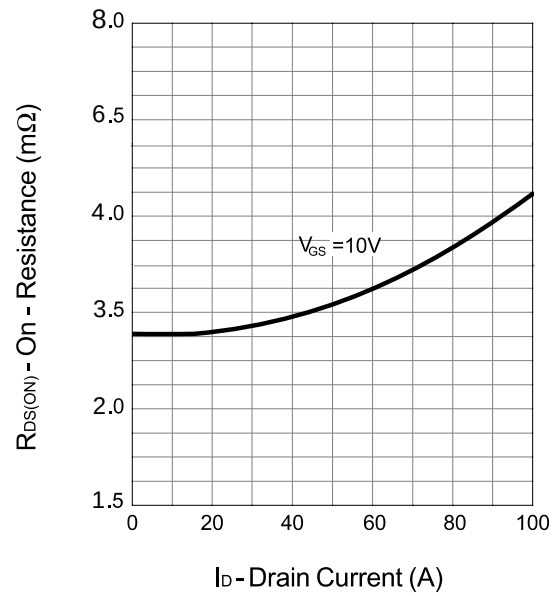


Typical Operating Characteristics (Cont.)

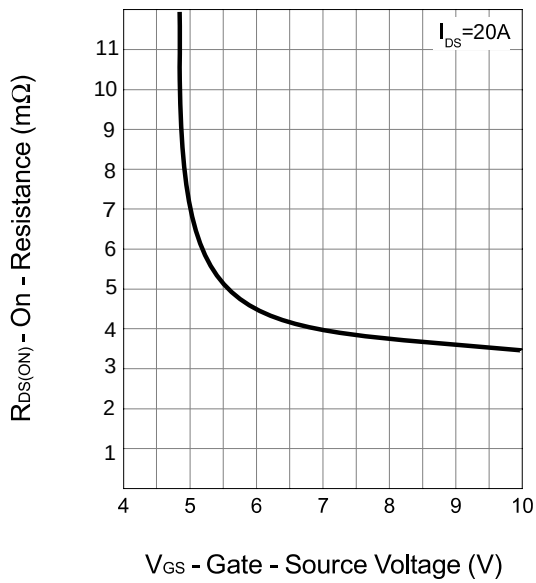
Output Characteristics



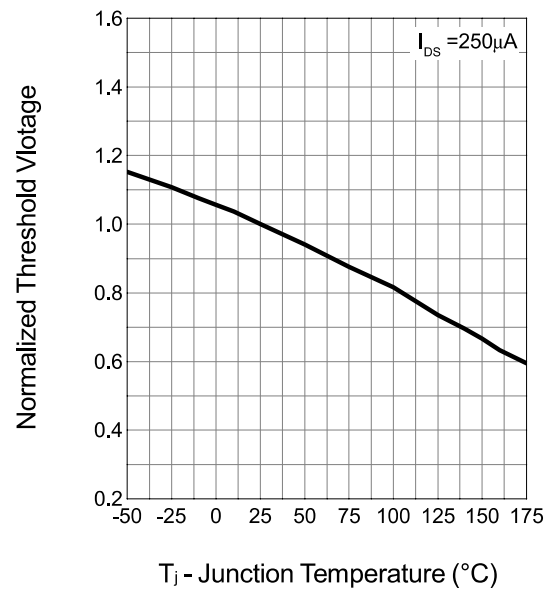
Drain-Source On Resistance



Drain-Source On Resistance

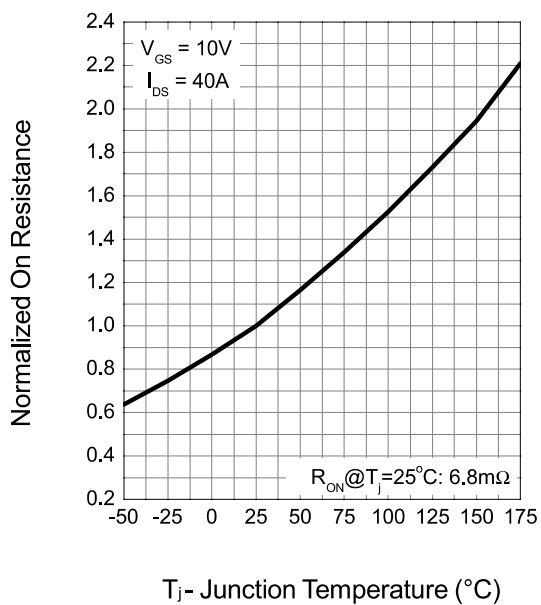


Gate Threshold Voltage

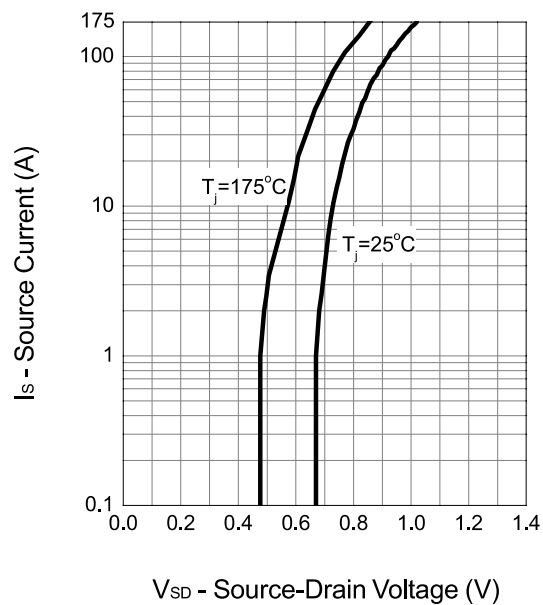


Typical Operating Characteristics (Cont.)

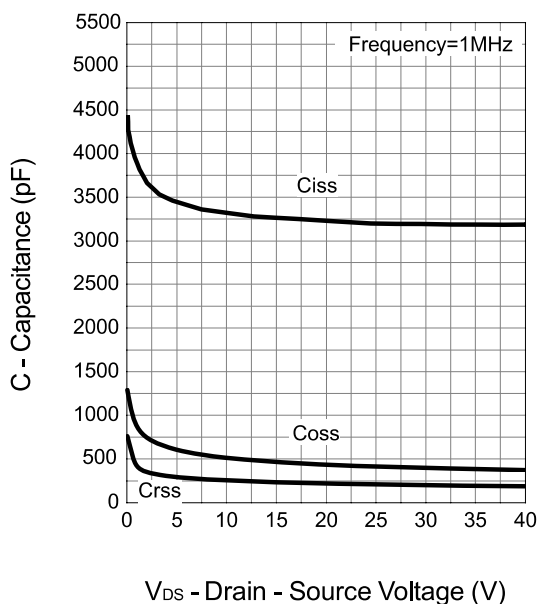
Drain-Source On Resistance



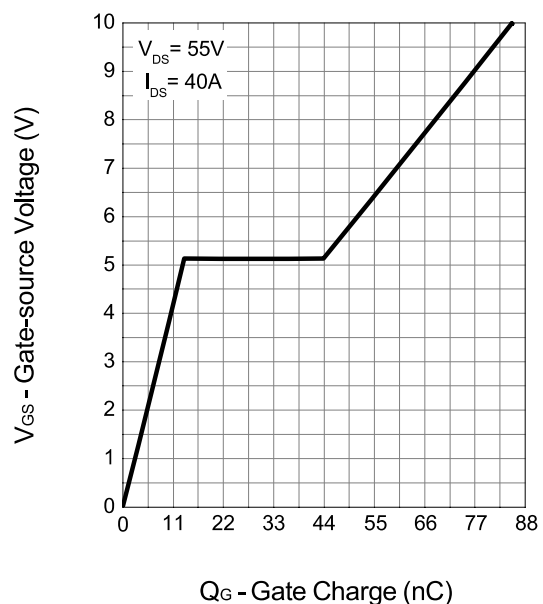
Source-Drain Diode Forward



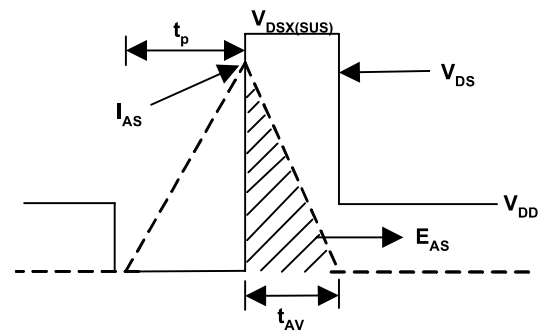
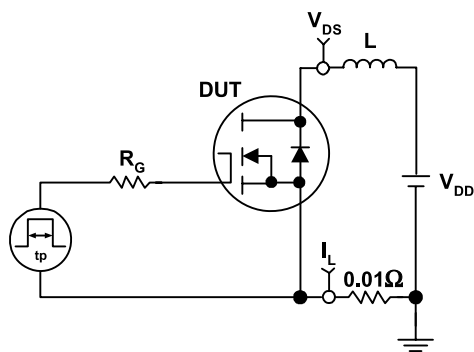
Capacitance



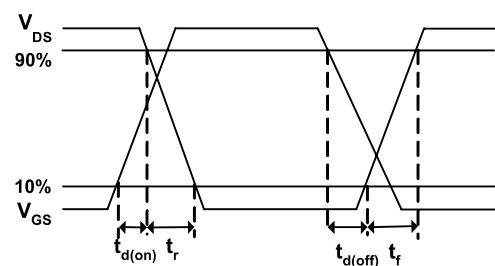
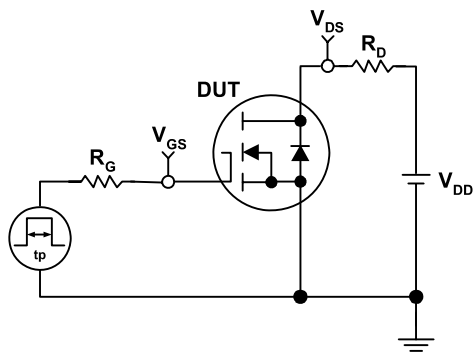
Gate Charge



Avalanche Test Circuit and Waveforms

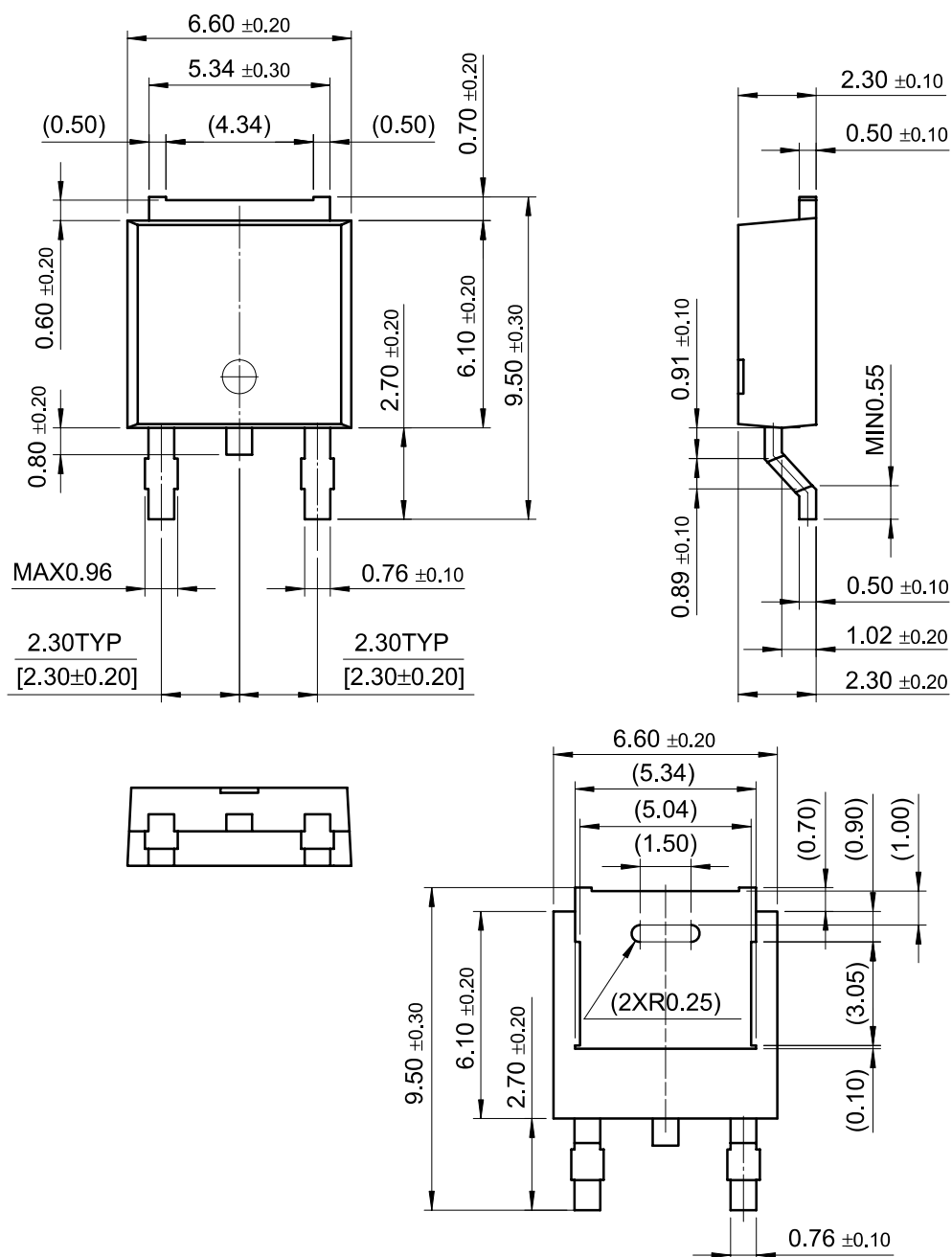


Avalanche Test Circuit and Waveforms



Package Dimensions

TO-252-2L



Dimensions in Millimeters

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