N-Channel Enhancement Mode Field Effect Transistor

Product Summary

PRODUCT SUMMARY				
Vdss	Id	$RDS(ON) (m \Omega) Typ$		
20V	90A	4 @ VGS=2.5V		
		2.5@ VGS=4.5V		

Features

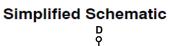
- Super high dense cell design for low RDS(ON)
- Rugged and reliable
- Simple drive requirement

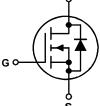
Applications

• LED Display

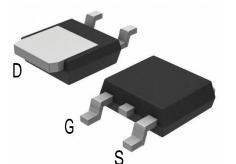
MT Semiconductor[®]

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MARKING DIAGRAM & PIN ASSIGNMENT



Absolute Maximum Ratings(T_A = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	20	V
Gate-Source Voltage	VGS	± 12	V
Drain Current-Continuous ^a @Tj=125°C	ID	90	А
- Pulse d^b	Ідм	270	А
Drain-source Diode Forward Current ^a	Is	125	А
Maximum Power Dissipation ^a	PD		
Operating Junction and Storage Temperature Range	Tj,Tstg	-55 to 150	°C

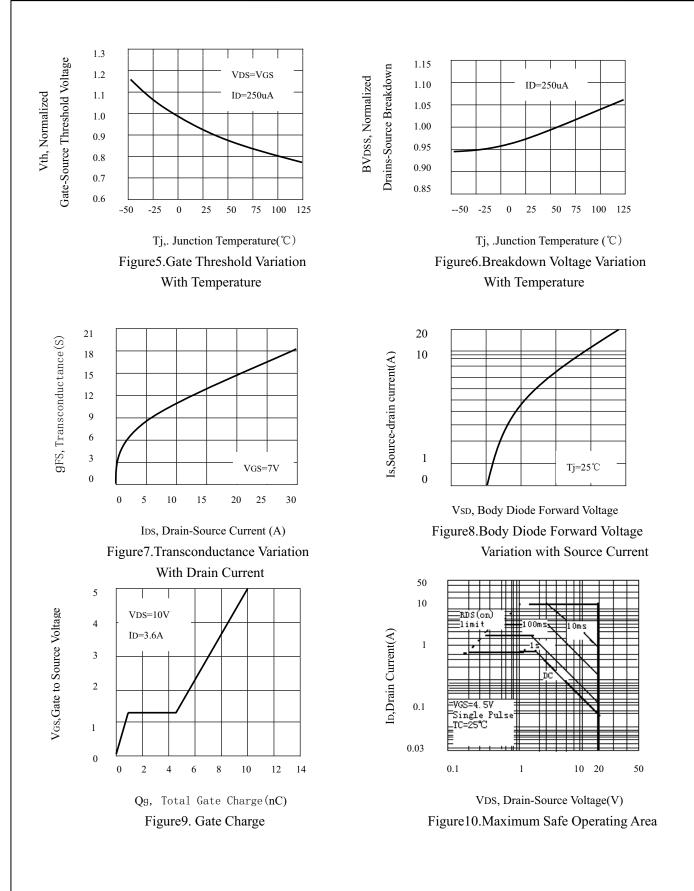
THERMAL CHARACTERISTICS

	Thermal Resistance, Junction-to Ambient ^a	Rth JA	100	°C/W
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Parameter	Symbol	Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS			1	1		I
Drain-Source Breakdown Voltage	BVDSS	Vgs=0V,Id=250µA	20			V
Zero Gate Voltage Drain Current	Idss	VDS=16V,VGS=0V			1	μA
Gate-Body Leakage	Igss	VGS=±8V,VDS=0V			±100	nA
ON CHARACTERITICS						
Gate Threshold Voltage	VGs(th)	VDS=VGS,ID=250µA	0.5	0.7	1.5	V
		VGs=2.5V,Ib=2.8A		3.8	4	
Drain-Source On-State Resistance	Rds(on)	VGs=4.5V,ID=2.0A		2.5	2.8	mΩ
Forward Transconductance	gfs	VGS=7V,ID=5A		5		S
DYNAMIC CHARACTERISTICS			1	I	1	1
Input Capacitance	Ciss			608		pF
Output Capacitance	Coss	V _{DS} =10V,V _{GS} =0V f=1.0MHz		115		pF
Reverse Transfer Capacitance	Crss	1-1.0101112		86		pF
SWITCHING CHARACTERISISTICS			1	I	1	1
Turn-On Delay Time	td(on)	V _{DD} =10V		10		ns
Rise Time	tr	ID=3.6A,		14		ns
Turn-Off Delay Time	td(off)	V _{GEN} =4.5V RL=100hm		39		ns
Fall Time	tf	Rgen=10ohm		26		ns
Total Gate Charge	Qg			9.2		nC
Gate-Source Charge	Qgs	VDS=10V,ID=1A		1.6		nC
Gate-Drain Charge	Qgd	VGS=4.5V		2.6		nC

Parameter	Symbol	Condition	Min	Тур	Max	Uni
DRAIN-SOURCE DIODE CHARACT	ERISTICS			1	1	
Diode Forward Voltage	Vsd	Vgs=0V,Is=1.25A		0.84	1.3	V
Notes Surface Mounted on FR4 Board, t \leq Pulse Test: Pulse Width \leq 300Us, Dr Guaranteed by design, not subject to 10 8 VGS=3V 4 VGS=10, 9, 8, 7, 6 4 2	uty Cycle≦2% production testin	Ig. 25 T i=125 20 15 15 10 5				
0 1 2 3 VDS, Drain-to-Sour Figure 1. Output Ch	-	0 0.5 VGS, G Figure 2.1 2.2 VGS=4V	1.0 1.5 ate-to-sou Gransfer C		ltage (N	
GS, Drain-to Source	Coss 20 25 30	$\begin{array}{c c} 1.8 \\ ID=3A \\ 1.4 \\ 1.0 \\ 0.2 \\ 0 \\ -50 \\ -25 \end{array}$	0 25	50 75	100 125 Tj(°C)	5



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TO252(DPAK)) PA	CKAGE	OUTLIN	E			
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3 2 1	s Y	DIMENS		METERS	DIME	NSIONS IN IN	CHES
╘╢┱╌╇╾╢┱	M B O	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
	A	2.184	2.286	2.388	0.086	0.090	0.094
ф ф	A1	0.000		0.127	0.000		0.005
	A2	0.889	1.041	1.143	0.035	0.041	0.045
	b	0.635	0.762	0.889	0.025	0.030	0.035
RECOMMENDED LAND PATTERN	b1	0.762	0.840	1.143	0.030	0.033	0.045
	b2	4.953	5.340	5.461	0.195	0.210	0.215
6.25 MIN.	C of	0.450	0.508	0.610	0.018	0.020	0.024
	c1 D	0.450 5.969	0.508 6.096	0.610 6.223	0.018 0.235	0.020	0.024
	D1	5.210	5.249	5.380	0.205	0.240	0.243
€	D2	0.662	0.762	0.862	0.026	0.030	0.034
	E	6.350	6.604	6.731	0.250	0.260	0.265
6.60	E1	4.318	4.826	4.901	0.170	0.190	0.193
	E2	1.678	1.778	1.878	0.066	0.070	0.074
+ 3.00 MIN.	е		2.286 BS	С		0.090 BS0	;
	e1		4.572 BS	iC		0.180 BS0	;
2.286 UNIT: mm	н	9.398	10.033	10.414	0.370	0.395	0.410
4.572 BSC	L	1.270	1.520	2.032	0.050	0.060	0.080
	L1		2.921 RE			0.115REF	
NOTE	L2	0.408	0.508	0.608	0.016	0.020	0.024
. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH SHOULD BE LESS THAN	L3	0.889	1.016	1.270	0.035	0.040	0.050
6 MILS.	L4	0.635		1.016	0.025		0.040
2. DIMENSION L IS MEASURED IN GAUGE PLANE							
3. TOLERANCE 0.10 mm UNLESS OTHERWISE SPECIFIED							
4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED							
4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.							
A. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED							

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